For our location in Gratkorn/Austria we are looking for a

Junior Analog Design Engineer (m/f)

NXP Semiconductors enables secure connections and infrastructure for a smarter world, advancing solutions that make lives easier, better and safer. As the world leader in secure connectivity solutions for embedded applications, we are driving innovation in the secure connected vehicle, end-to-end security & privacy and smart connected solutions markets.

Your Responsibilities:

• Analog design of integrated circuit for NFC applications
• Interpret block specifications to achieve performance targets
• Commitment to top-level chip verification
• Implement DFT strategy to achieve competitive test time
• Support test development engineers for production ramp up
• Participate regular peer to peer reviews and design reviews

Your Profile:

• Master Degree in Electrical/Electronic engineering or similar
• Take responsibility for NFC products and drive leading edge solutions
• Meet agreed goals under pressure
• Engaged, self-motivated, flexible and enthusiastic team player

NXP offers competitive compensation. Due to Austrian law we are obliged to state the minimum gross salary according to legal regulations and for this role this amounts to EUR 44,500 gross. Depending on experience and education higher remuneration is possible. Moreover, we provide attractive benefits to our employees.